



**Sri Satya Sai University of Technology & Medical Sciences,
Sehore (M.P.)**

Scheme of Examination

Fourth Semester –M.Tech. (VLSI Design)

S.No	Subject Code	Subject Name	Credits Allotted Subject wise/ Period per week			Credits	Maximum marks (Theory Slot)			Maximum Marks (Practical Slot)		Total Marks
			L	T	P		End Sem. Exam.	Tests (Two)	Assignments/Quiz	End Sem. Practical / Viva	Practical Record/assignment/Quiz/Presentation	
1.	MEVD 401	Dissertation Part- II	-	-	20	20	-	-	-	300	200	500
		Total	-	-	20	20	-	-	-	300	200	500

L: Lecture- T: Tutorial- P: Practical

w.e.f. July- 2015