



Sri Satya Sai University of Technology & Medical Sciences, Sehore (M.P)

Scheme of Examination - CBCS Pattern

Academic Year 2017- 2018

Branch : Electronics and Communications Engineering

Semester - V

S.No.	Subject Code	Subject Name	Maximum Marks Theory Slot			Maximum Marks (Practical Slot)		Periods/ hour/ week			Credits	Total Marks
			End Sem. Exam.	Mid Tests	Assign-ments/Quiz	End Sem. Practical & Viva	Practical Record /Assignment/ Quiz / Presentation	L	T	P		
1	ECC - 501	Digital Communication	60	30	10	30	20	2	1	2	4	150
2	ECC - 502	Digital Signal Processing	60	30	10	30	20	2	1	2	4	150
3	ECC - 503	VLSI Design	60	30	10	30	20	2	1	2	4	150
4	ECC - 504	Department Elective-I	60	30	10	-	-	2	1	-	3	100
5	ECC - 505	Department Elective-II	60	30	10	-	-	2	1	-	3	100
6	ECC - 506	Open Elective	60	30	10	-	-	2	1	-	3	100
7	ECC - 507	Industrial Training - I					100			4	2	100
TOTAL			360	180	60	90	160	12	6	10	23	850
Department Elective I-ECC -504			ECC-504 (A)Neural Network			ECC-504(B) Control System		ECC-504(C) Data Compression & Cryptography				
Department Elective II-ECC-505			ECC-505(A)Telecom Switching Systems			EC -505(B) Design of Analog IC's		ECC-505(C)Industrial Electronics				
Open Elective -ECC- 506			ECC-506(A) Operating System			ECC-506(B)Biometric Signal		ECC-506(C) Advance Instrumentation				

W.E.F.JULY-2018