



Sri Satya Sai University of Technology & Medical Sciences, Sehore (M.P)

Scheme of Examination - CBCS Pattern

Academic Year 2017- 2018

Branch : Electronics and Communications Engineering

Semester - VI

S.No.	Subject Code	Subject Name	Maximum Marks Theory Slot			Maximum Marks (Practical Slot)		Periods/ hour/ week			Credits	Total Marks
			End Sem. Exam.	Mid Tests	Assign-ments/Quiz	End Sem. Practical & Viva	Practical Record /Assignment/ Quiz / Presentation	L	T	P		
1	ECC - 601	Optical Communication	60	30	10	30	20	2	1	2	4	150
2	ECC - 602	Microwave Engineering	60	30	10	30	20	2	1	2	4	150
3	ECC - 603	Digital Image Processing	60	30	10	30	20	2	1	2	4	150
4	ECC - 604	Department Elective-III	60	30	10	-	-	2	1	-	3	100
5	ECC - 605	Department Elective-IV	60	30	10	-	-	2	1	-	3	100
6	ECC - 606	Open Elective	60	30	10	-	-	2	1	-	3	100
7	ECC - 607	Industrial Training Project - I	-	-	-	100	-	-	-	4	2	100
TOTAL			360	180	60	190	60	12	6	10	23	850
Department Elective III-ECC -604			ECC-604(A) Digital System Design			ECC-604(B) Design for Testability		ECC-604(C) Radio Frequency Identification				
Department Elective IV-ECC-605			ECC-605(A) Global Positioning System			ECC-605(B) Computer Network		ECC-605(C) Safety & Reliability Engineering				
Open Elective -ECC-606			ECC-606(A) TCP-IP			ECC-606(B) Fuzzy Logic		ECC-606(c) Telecom Technology Management Processing				

W.E.F.JULY-2018