



Sri Satya Sai University of Technology & Medical Sciences, Sehore (M.P)

Scheme of Examination - CBCS Pattern

Electronics and Instrumentation Engineering

VII Semester

S.No.	Subject Code	Subject Name	Maximum Marks Theory Slot			Maximum Marks (Practical Slot)		Periods/ hour/ week			Credits	Total Marks
			End Sem. Exam.	Mid Tests	Assign-ments/Quiz	End Sem. Practical & Viva	Practical Record /Assignment/ Quiz / Presentation	L	T	P		
1	EIC-701	Optical Instruments & Sensors	60	30	10	30	20	2	1	2	4	150
2	EIC-702	Virtual Instrumentation	60	30	10	30	20	2	1	2	4	150
3	EIC-703	Industrial Instrumentation	60	30	10	30	20	2	1	2	4	150
4	EIC-704	Departmental Elective-V	60	30	10			2	1		3	100
5	EIC-705	Departmental Elective-VI	60	30	10			2	1		3	100
6	EIC- 706	Open Elective	60	30	10			2	1		3	100
7	EIC- 707	Industrial Training - II					100			4	2	100
TOTAL			360	180	60	90	160	12	6	10	23	850
Departmental Elective V-EIC -704			EIC-704(A) Nano Science			EIC-704(B) Microcontrollers for embedded system		EIC-704(C) Analog Signal Processing				
Departmental Elective VI-EIC-705			EIC-705(A) Space Science			EIC-705(B) LASER System and Applications		EIC-705 (C) Intelligent Instrumentation				
Open Elective -EIC-706			EIC-706(A) Entrepreneurship Development			EIC-706(B) Artificial Intelligence		EIC-706(C) Data Compression				