



Sri Satya Sai University of Technology & Medical Sciences, Sehore (M.P)

Scheme of Examination - CBCS Pattern

Academic Year 2019- 2020

Branch :Electrical Engineering

Semester - VII

S.No.	Subject Code	Subject Name	Maximum Marks Theory Slot			Maximum Marks (Practical Slot)		Periods/ hour/ week			Credits	Total Marks
			End Sem. Exam.	Mid Tests	Assign-ments/Quiz	End Sem. Practical & Viva	Practical Record /Assignment/ Quiz / Presentation	L	T	P		
1	EEC- 701	Power System protection	60	30	10	30	20	2	1	2	4	150
2	EEC- 702	Generalized Theory of Electrical Machines	60	30	10	30	20	2	1	2	4	150
3	EEC- 703	Computer Aided Design of Electrical machine	60	30	10	30	20	2	1	2	4	150
4	EEC- 704	Department Elective-V	60	30	10			2	1		3	100
5	EEC- 705	Department Elective-VI	60	30	10			2	1		3	100
6	EEC- 706	Open Elective	60	30	10			2	1		3	100
7	EEC- 707	Industrial Training - II					100			4	2	100
TOTAL			360	180	60	90	160	12	6	10	23	850
Department Elective V-EEC -704			EEC-704(A) Solar PV Application			EEC-704(B) Power Quality		EEC-704(C) Entrepreneurship Development				
Department Elective VI-EEC-705			EEC-705(A) Digital signal procesing			EEC-705(B) Advanced Microprocessor		EEC-705(C) Demand side management				
Open Elective -EEC- 706			EEC-706(A) System Engineering			EEC-706(B) Embedded System		EEC-706(C) Fuzzy Logic System				

SSSUTMS

w.e.f July 2019