

SCHOOL OF ENGINEERING
SRI SATYA SAI UNIVERSITY OF TECHNOLOGY AND MEDICAL SCIENCES
Scheme of Examination - AICTE Pattern
Undergraduate Degree Courses in Engineering & Technology
Bachelor of Engineering (Electrical Engineering)

VIIth SEMESTER

S.No	Subject Code	Subject Name	Maximum Marks Theory Slot			Maximum Marks (Practical Slot)		Total Marks	Periods/ hour/ week			Credits
			End Sem. Exam	Mid Tests	Assign-ments/ Quiz	End Sem. Practical & Viva	Practical Record /Assignment/ Quiz / Presentation		L	T	P	
1	EEA-701	Power System Protection	60	30	10	30	20	150	3	0	2	4
2	EEA-702	Electrical Drives	60	30	10	30	20	150	3	0	2	4
3	EEA-703	Program Elective-IV	60	30	10			100	3	0	0	3
4	EEA-704	Open Core Elective - III	60	30	10		-	100	3	0	0	3
5	EEA-705	Project Stage-I	-	-	-	120	80	200	-	-	10	5
6	EEA-706	Self-Study/GD/Seminar					200	200			2	1
		TOTAL	240	120	40	180	320	900	12	0	16	20

Program Elective-IV	
EEA-703	EEA-703 (A) High Voltage Engineering
	EEA-703 (B) Power Quality and FACTS
	EEA-703(C) Image Processing
Open Core Elective-III	
EEA-704	EEA-704 (A) VLSI circuits
	EEA-704 (B) Evolutionary Techniques
	EEA-704(C) Data Structures and Algorithms